Application Serial No. 10/044,487 - Filed January 11, 2002

REPLY UNDER 37 C.F.R. § 1.116 EXPEDITED PROCEDURE EXAMINING GROUP 2183

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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10/044,487 Application No.: Examiner: Huisman, David J. Group/Art Unit: 2183 Filed: January 11, 2002 Atty. Dkt. No: 5181-37601 Inventor(s): 888 I hereby certify that this correspondence is being sent via facsimile to: Facsimile No. 703-872-9306, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the Bodo K. Parady Rory D. Rankin Title: Load Address Prediction And New Thread February 4, 2005 Identification In A Date Multithreaded Microprocessor

RESPONSE TO ADVISORY ACTION OF JANUARY 28, 2005

MAIL STOP AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Advisory Action of January 28, 2005, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.